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<td>2029</td>
<td>Feature</td>
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<td>Add v6.9.0 release to Spack</td>
<td>Matthias Diener</td>
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<td>1996</td>
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<td>Ckloop hybrid mode and drone support</td>
<td>Kavitha Chandrasekar</td>
<td>10/19/2018 08:50 AM</td>
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<td>1971</td>
<td>Feature</td>
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<td>Explore use of non-temporal stores in the RTS</td>
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<td>1952</td>
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<td>Pass CkNcpyBuffer instead of a separate CkNcpyAck object in the callback invoked after completion of a zero copy data transfer</td>
<td>Nitin Bhat</td>
<td>08/07/2018 04:04 PM</td>
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<td>1921</td>
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<td>Make Isomalloc/the GNI mempool not use the pool for large allocations</td>
<td>Evan Ramos</td>
<td>12/18/2016 03:55 AM</td>
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<td>1920</td>
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<td>Normal</td>
<td>Make mempool aware of Isomalloc's per-slot metadata</td>
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<td>Zero copying Direct API: Allow user to pass an arbitrary reference pointer into CkNcpyBuffer which can be later accessed in the callback</td>
<td>Nitin Bhat</td>
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<td>1909</td>
<td>Feature</td>
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<td>Normal</td>
<td>Proper support for callbacks to [immediate] entry methods of node groups</td>
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<td>05/17/2016 03:54 PM</td>
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<td>1882</td>
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<td>Print slightly more detailed CPU topology info at startup by default</td>
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<td>Test whether hwloc can be used via a git submodule in charm++</td>
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<td>Update Boost ULTs to v1.67.0</td>
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<td>Charm++ type traits for compile-time querying of types</td>
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<td>PUP support for templated abstract base classes</td>
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<td>Direct API - Support multiple user operation modes</td>
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<td>Direct API for the OFI layer</td>
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<td>Direct API - Add CMA support for netlrls, multicore and pami builds</td>
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<td>Direct API for the generic layer (non-rdma based default implementation)</td>
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<td>1778</td>
<td>Feature</td>
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<td>Clean and portable way of generating a working libcharm.so for charmpy</td>
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<td>05/16/2018 04:07 PM</td>
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<td>Feature</td>
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<td>Normal</td>
<td>Add an ampinrun script as drop-in replacement for mpiexec</td>
<td>Matthias Diener</td>
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<td>CharmPy support in Charm++</td>
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<td>Interface to reductions taking std::vector instead of a bare pointer</td>
<td>Phil Miller</td>
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<td>1739</td>
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<td>Normal</td>
<td>Improve PUP performance of std::vector&lt;T&gt; where T is PUPbytes</td>
<td>Sam White</td>
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<td>1731</td>
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<td>Complete spack installation script</td>
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<td>1730</td>
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<td>The RTS should set std::set_terminate</td>
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<td>Make Boost uFcontext the default ULT implementation on supported platforms</td>
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<td>1723</td>
<td>Feature</td>
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<td>Normal</td>
<td>Rebase the OpenMP version onto the latest version of LLVM runtime library</td>
<td>Seonmyeong Bak</td>
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<td>pxshm for mpi layer</td>
<td>Sam White</td>
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<td>pxshm in OFI</td>
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<td>11/10/2017 09:12 AM</td>
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<td>Avoid intermediate ctx to scheduler in case of ULTs</td>
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<td>Add a pamirts-linux-ppc64le build target</td>
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<td>1682</td>
<td>Feature</td>
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<td>Normal</td>
<td>Expose Arrays' Index Type as a Type Alias</td>
<td>Evan Ramos</td>
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<td>Basic OFI (libfabric) network layer using PSM transport</td>
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<td>1672</td>
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<td>LLDB support for charmrun ++debug</td>
<td>Evan Ramos</td>
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<td>1670</td>
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<td>Merged</td>
<td>Normal</td>
<td>Migration of Taskqueue from Ck to Converse layer</td>
<td>Seonmyeong Bak</td>
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<td>1667</td>
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<td>Normal</td>
<td>Direct API for nocopy operations on sender-side and receiver-side</td>
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<td>1657</td>
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<td>CMA support for nocopy sends using the Entry Method API across processes on the same host</td>
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<td>12/11/2018 04:15 PM</td>
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<td>1655</td>
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<td>Enable use of shm transport for regular messages in LRTS</td>
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<td>1637</td>
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<td>Zero-copy send support in the OFI layer</td>
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<td>1631</td>
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<td>AMPI lacks API to clear LB database</td>
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<td>1626</td>
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<td>Modify the keyword &quot;rdma&quot; to &quot;nocopy&quot;</td>
<td>Nitin Bhat</td>
<td>07/11/2017 02:34 PM</td>
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<td>Normal</td>
<td>User-level thread implementation based on Boost context library</td>
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<td>1584</td>
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<td>Allow exit codes in CkExit() / ConverseExit()</td>
<td>Matthias Diener</td>
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<td>1579</td>
<td>Feature</td>
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<td>Mark CkAbort [[noreturn]]</td>
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<td>The OpenMP integration modified to run on Converse user-level threads</td>
<td>Seonmyeong Bak</td>
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<td>Make HDF5 library available for AMPI</td>
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<td>Support the Flang Fortran compiler</td>
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<td>Better support for AMPI/Projections with multiple virtual ranks</td>
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<td>RDMA example with migration</td>
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<td>Example for using Google benchmark library with Charm++</td>
<td>Dong Hun Lee</td>
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<td>Enable automated testing of tracemodes</td>
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<td>Improve multi-GPU support</td>
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<td>Broadcast trees are not topology-aware</td>
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<td>Investigate use of pxshm in CmiAlloc</td>
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<td>Don't require migration constructors for all array objects at compile time</td>
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<td>Zero-copy send support for the MPI machine layer</td>
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<td>Default option to choose between isomalloc and os-isomalloc</td>
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<td>Add more stream callbacks for use after HToD transfer and kernel execution</td>
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<td>Clean up and add CUDA example programs</td>
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<td>General Implementation for Serializing Enums</td>
<td>Eric Mikida</td>
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<td>Optimize degenerate CkLoop cases</td>
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<td>Lambda syntax for CkLoop</td>
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<td>Lockless queue build option --enable-lockless-queue (off by default)</td>
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<td>Redesign of Hybrid API (GPU Manager) to support concurrent kernel execution</td>
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<td>AMPI ATARq test/wait performs poorly</td>
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<td>CkDestroy for Groups and NodeGroups</td>
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<td>Add streaming floating point sum reducers</td>
<td>Ronak Buch</td>
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<td>AMPI thread should not be awoken unless progress can be made</td>
<td>Sam White</td>
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<td>Add AMPI RMA routine tests to megampi</td>
<td>Sam White</td>
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<td>1353</td>
<td>Feature</td>
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<td>Normal</td>
<td>Charmc hardcodes unversioned compiler names</td>
<td>Ronak Buch</td>
<td>03/11/2018 01:17 AM</td>
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<td>1352</td>
<td>Feature</td>
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<td>Urgent</td>
<td>CkArrayOptions callback for completion of chare array initialization</td>
<td>Phil Miller</td>
<td>05/14/2017 09:53 AM</td>
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<td>1351</td>
<td>Feature</td>
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<td>Normal</td>
<td>For broadcasts, avoid copies for destination PEs on same node for [nokeep] messages</td>
<td>Vipul Harsh</td>
<td>04/13/2017 03:02 PM</td>
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<td>1322</td>
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<td>PSM2 network layer</td>
<td>Nitin Bhat</td>
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<td>1298</td>
<td>Feature</td>
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<td>Normal</td>
<td>Build MetisLB by default</td>
<td>Kavitha Chandrasekar</td>
<td>04/11/2017 03:59 PM</td>
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<td>1297</td>
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<td>Normal</td>
<td>Patch METIS to privatize global variables</td>
<td>Kavitha Chandrasekar</td>
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<td>1295</td>
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<td>Reduce overheads of message creation in the OpenMP integration by adjusting the number of OpenMP tasks</td>
<td>Seonmyeong Bak</td>
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<td>1292</td>
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<td>Alternatives to linked-list message queues in AMPI</td>
<td>Sam White</td>
<td>11/11/2018 05:11 PM</td>
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<td>1243</td>
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<td>Normal</td>
<td>Within-node work stealing queues in Converse</td>
<td>Seonmyeong Bak</td>
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<td>1283</td>
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<td>Onesided sender side implementation for Verbs layer</td>
<td>Jaemin Choi</td>
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<td>Onesided sender side implementation for GNI layer</td>
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<td>Onesided sender side implementation for PAMI layer</td>
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<td>1234</td>
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<td>Low</td>
<td>Avoid sender-side copy for large contiguous messages. API for charm and converse layers</td>
<td>Vipul Harsh</td>
<td>09/20/2018 01:29 PM</td>
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<td>1233</td>
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<td>Normal</td>
<td>LLVM OpenMP runtime integration with Converse</td>
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<td>1231</td>
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<td>Use a single GPU manager within each shared-memory process</td>
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<td>Automate simple uses of TRAM small-message aggregation</td>
<td>Phil Miller</td>
<td>04/14/2017 11:09 AM</td>
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<td>1218</td>
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<td>Normal</td>
<td>Move production features out of &quot;experimental&quot; part of Charm++ Manual</td>
<td>Sam White</td>
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<td>1213</td>
<td>Feature</td>
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<td>Normal</td>
<td>Use Charm++ built-in reducers for more predefined types/ops in AMPI</td>
<td>Sam White</td>
<td>09/23/2016 08:16 AM</td>
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<td>1187</td>
<td>Feature</td>
<td>Merged</td>
<td>Normal</td>
<td>Automatic delegation of section work to CkMulticastMgr</td>
<td>Vipul Harsh</td>
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<td>1181</td>
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<td>Normal</td>
<td>Support automated process launch on a single host for standalone runs</td>
<td>Evan Ramos</td>
<td>06/21/2018 04:03 PM</td>
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<td>1180</td>
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<td>Support automated thread spawn/bind for standalone runs</td>
<td>Evan Ramos</td>
<td>06/21/2018 04:06 PM</td>
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<td>Merged</td>
<td>High</td>
<td>Support automated launch/spawn/bind when using charmrun ++mpiexec</td>
<td>Evan Ramos</td>
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<td>1170</td>
<td>Feature</td>
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<td>High</td>
<td>Detect unsupported non-uniformity of processes/threads in charmrun, and error</td>
<td>Evan Ramos</td>
<td>06/21/2018 04:05 PM</td>
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<td>1173</td>
<td>Feature</td>
<td>Merged</td>
<td>High</td>
<td>Don't require autoconf to be installed on user systems for hwloc build</td>
<td>Evan Ramos</td>
<td>10/27/2017 04:16 PM</td>
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<td>1170</td>
<td>Feature</td>
<td>Merged</td>
<td>Normal</td>
<td>Add option to make all TCharm entry methods [expedited]</td>
<td>Sam White</td>
<td>08/23/2016 09:31 AM</td>
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<td>1164</td>
<td>Feature</td>
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<td>Normal</td>
<td>AMPI support for MPI-3's request-based RMA routines</td>
<td>Sam White</td>
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<td>1133</td>
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<td>PMPI_inter for AMPI</td>
<td>Matthias Diener</td>
<td>07/19/2018 09:31 AM</td>
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<td>1114</td>
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<td>Build and link ROMIO on AMPI by default</td>
<td>Sam White</td>
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<td>1113</td>
<td>Feature</td>
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<td>Normal</td>
<td>smart-build.pl should detect supercomputers with specialized software environments (BG/Q, Cray) and act accordingly</td>
<td>Evan Ramos</td>
<td>03/26/2018 03:29 PM</td>
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<td>Avoid sender-side copy in AMPI for large contiguous messages</td>
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<td>1099</td>
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<td>Normal</td>
<td>Use built-in reducer types for basic types/operations in AMPI</td>
<td>Sam White</td>
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<td>1098</td>
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<td>Mark AMPI's reduction result entry method's [nokeep]</td>
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<td>1086</td>
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<td>Trace MPI_functions in AMPI</td>
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<td>AMPI topologies should find neighbor ranks during construction</td>
<td>Sam White</td>
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<td>1083</td>
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<td>AMPI message logging support missing for some routines</td>
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<td>Migrate solaris builds from net to netltrs</td>
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<td>Normal</td>
<td>Migrate net-linux-ppc to netltrs</td>
<td>Sam White</td>
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<td>Migrate net-linux-arm7 to netltrs</td>
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<td>Sam White</td>
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<td>Normal</td>
<td>Migrate lagging 'net' builds to 'netltrs'</td>
<td>Phil Miller</td>
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<td>Make all AMPI entry methods [expedited]</td>
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<td>Edward Hutter</td>
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<td>1008</td>
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<td>Sam White</td>
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<td>988</td>
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<td>Normal</td>
<td>charmrun should not ignore +ppn</td>
<td>Evan Ramos</td>
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<td>985</td>
<td>Feature</td>
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<td>Make AMPI_Gather and its variants use Tuple/Set Reductions</td>
<td>Sam White</td>
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<td>AMPI support for missing MPI-3 non-blocking collectives</td>
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<td>982</td>
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<td>Gather reduction operation using Tuple/Set reducers</td>
<td>Steve Hoelle</td>
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<td>975</td>
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<td>multicores: spawn a thread per core by default</td>
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<td>967</td>
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<td>AMPI support for MPI-3’s MPI_Comm_create_group</td>
<td>Edward Hutter</td>
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<td>963</td>
<td>Feature</td>
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<td>Runtime flag to silence runtime output</td>
<td>Juan Galvez</td>
<td>04/11/2016 12:51 PM</td>
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<td>954</td>
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<td>Update AMPI’s version of MPICH test suite</td>
<td>Sam White</td>
<td>12/10/2018 11:50 AM</td>
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<td>950</td>
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<td>AMPI support for MPI-2’s Win related functions</td>
<td>Sam White</td>
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<td>949</td>
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<td>Sam White</td>
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<td>AMPI compliant no-op implementation of MPI-2’s Spawn and Spawn_multiple</td>
<td>Sam White</td>
<td>01/22/2016 08:34 AM</td>
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<td>MPI_WIN_BASE and other MPI-2.2 macros not defined in AMPI</td>
<td>Sam White</td>
<td>01/18/2016 09:13 PM</td>
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<td>Efficient yield in AMPI_Waitany and AMPI_Waitsome</td>
<td>Sam White</td>
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<td>Normal</td>
<td>Add ‘-default-to-aout’ flag to AMPI compilers by default</td>
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<td>Feature</td>
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<td>Normal</td>
<td>Isomalloc: refactor to associate migratable allocations with entities other than persistent threads</td>
<td>Evan Ramos</td>
<td>10/02/2018 04:50 PM</td>
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<td>933</td>
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<td>Get rid of unnecessary yields within AMPI_Test* and AMPI_Wait* routines</td>
<td>Sam White</td>
<td>02/10/2016 09:44 AM</td>
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<td>915</td>
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<td>option to call mpiexec without passing -n proc</td>
<td>Jim Phillips</td>
<td>12/07/2015 12:47 PM</td>
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<td>support ++mpiexec with poe</td>
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<td>Add Support for Execution on a Specific PE to CkLoop</td>
<td>Harshitha Menon</td>
<td>12/26/2015 12:50 PM</td>
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<td>891</td>
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<td>Normal</td>
<td>AMPI versions of ckAboutToMigrate and ckJustMigrated</td>
<td>Sam White</td>
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<td>886</td>
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<td>Add option to toggle comm thread tracing</td>
<td>Ronak Buch</td>
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<td>882</td>
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<td>Merged</td>
<td>Normal</td>
<td>Change type to indicate non-participation in reductions</td>
<td>Juan Galvez</td>
<td>09/28/2016 01:32 PM</td>
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<td>873</td>
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<td>send messages from non-PE threads</td>
<td>Michael Robson</td>
<td>09/27/2018 12:22 PM</td>
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<td>869</td>
<td>Feature</td>
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<td>Normal</td>
<td>Merge PICS framework to Charm</td>
<td>Harshitha Menon</td>
<td>02/01/2017 12:29 PM</td>
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<td>831</td>
<td>Feature</td>
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<td>Normal</td>
<td>Reject duplicate command line arguments that don’t make sense to repeat</td>
<td>Seonmyeong Bak</td>
<td>04/14/2016 03:27 PM</td>
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<td>813</td>
<td>Feature</td>
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<td>High</td>
<td>Publish GPU manager documentation</td>
<td>Michael Robson</td>
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<td>808</td>
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<td>Implement a communication aware load balancer</td>
<td>Harshitha Menon</td>
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<td>793</td>
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<td>Write default cuda kernelSelect so cuda build works out of the box</td>
<td>Michael Robson</td>
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<td>Feature</td>
<td>Merged</td>
<td>Normal</td>
<td>Charmrun should test for SSH failures when node programs fail to launch</td>
<td>Evan Ramos</td>
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<td>712</td>
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<td>Enable user-specified mapping of AMPI ranks to cores</td>
<td>Sam White</td>
<td>01/16/2017 10:36 PM</td>
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<td>693</td>
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<td>Normal</td>
<td>add CcdCallBacksReset() to header file (or improve callback frequency)</td>
<td>Michael Robson</td>
<td>10/03/2016 09:33 PM</td>
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<td>680</td>
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<td>reliable inter-partition barrier</td>
<td>Nikhil Jain</td>
<td>12/03/2015 05:20 PM</td>
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<td>656</td>
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<td>Normal</td>
<td>Make -E option default to enable macros in .ci files</td>
<td>Eric Mikida</td>
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<td>convert Cmldirect usage to CmliOneSided</td>
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<td>add CkLoop to standard build</td>
<td>Harshitha Menon</td>
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<td>Increase version macro to 60601</td>
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<td>pe threads sleep on condition variables</td>
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<td>574</td>
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<td>Low</td>
<td>Windows builds: don't pollute /usr/bin</td>
<td>Michael Robson</td>
<td>09/28/2016 01:32 PM</td>
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<td>557</td>
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<td>Normal</td>
<td>Built-in reducer types for logical and bitvec and/or/xor on bools and ints</td>
<td>Sam White</td>
<td>10/31/2016 10:49 AM</td>
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<td>554</td>
<td>Feature</td>
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<td>Normal</td>
<td>Bulk construction for regular sparse arrays</td>
<td>Eric Mikida</td>
<td>10/27/2015 11:37 AM</td>
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<td>541</td>
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<td>SMP message passing must enforce memory ordering consistency</td>
<td>Nitin Bhat</td>
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<td>Add new image output formats</td>
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<td>Automatically determine location of CUDA toolkit when building GPU Manager</td>
<td>Lukasz Wesolowski</td>
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<td>save processor data set</td>
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<td>extend +pemap notation to interleave hyperthreads</td>
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<td>Reduction for stats collection</td>
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<td>Display warning if user tries processing large logs</td>
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<td>Change Default Extrema Analysis View to &quot;Least Idle Time&quot;</td>
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<td>PXSHM: use external information instead of +nodesize runtime argument</td>
<td>Ralf Gunter Corrêa Carvalho</td>
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<td>KillOnAllSigs should print partition ID in partitioned runs</td>
<td>Phil Miller</td>
<td>02/04/2014 05:04 PM</td>
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<td>charmrun_abort should print partition ID in partitioned runs</td>
<td>Yanhua Sun</td>
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<td>CmiAbort should print partition ID in partitioned runs</td>
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<td>In partitioned runs, abort message should include partition ID</td>
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<td>TRAM: add a class to specify the routing interface and refactor existing routing code</td>
<td>Lukasz Wesolowski</td>
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<td>Investigate implementation of CCS on BG/Q</td>
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<td>Configure-time test for compiler variadic template constructor support</td>
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<td>new test case for the &quot;case&quot; SDAG statement</td>
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<td>Add &quot;return value marshalling&quot; to entry methods</td>
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<td>Intranode optimizations for TRAM</td>
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<td>Return any type that has a PUP routine from [sync] entry methods</td>
<td>Vipul Harsh</td>
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<td>Message queue randomization for message-order debugging</td>
<td>Harshitha Menon</td>
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<td>Normal</td>
<td>Target files for netlrs-darwin-x86(_64)</td>
<td>Nikhil Jain</td>
<td>11/06/2013 01:50 PM</td>
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<td>Added parsing logic so that ++debug-commands can be used with several gdb commands separated by &quot;\n&quot;</td>
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<td>AMPI Privatization: Structure-building source-source transformation</td>
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<td>MPI_Get_version not implemented</td>
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<td>Record the size each object's migration data in the LB database</td>
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<td>MetaBalancer: add mechanism to inform user of AtSync call that resulted in actual load balancing</td>
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<td>Specify a reference number for messages sent through callbacks</td>
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<td>Charmrun wrapper around ibrun.symm for dual binary execution on host+mic with IMPI on stampede</td>
<td>PPL</td>
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<td>Intel MPI dual binary method for execution on stampede</td>
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<td>Charm++ Shrink-Expand (malleable jobs, dynamically varying node count)</td>
<td>Bilge Acun</td>
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<td>Reduce SDAG copies when possible</td>
<td>Jonathan Lifflander</td>
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<td>Performance tracking of ping pong over time</td>
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<td>Modify old charm++/converse onedison/direct API and examples to new rdma API</td>
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<td>objid_t: load balancing infrastructure should use objid_t</td>
<td>Kavitha Chandrasekar</td>
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<td>objid_t: location cache should be keyed by objid_t</td>
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<td>objid_t: Distributed lookup protocol</td>
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<td>objid_t: Specify array size at construction time via CkArrayOptions despite non-bulk-construction</td>
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<td>objid_t: Basic definitions and use in envelope</td>
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<td>ensure charm builds with C11/C+++11 flags</td>
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<td>Graph performance components from autobuild</td>
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<td>Architecture target for Cray XC in the stable branch.</td>
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<td>Partition according to topology, not linear node numbering</td>
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<td>Fixed-size object IDs</td>
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<td>Reduce the impact of large messages sent to unlocated or migrated chare array elements</td>
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<td>MetaLB: Separate cost/benefit analysis for running LB strategy and performing migration</td>
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<td>Verify that compress works with envelope reduction branch</td>
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<td>Make CkIO safe to use in real application code with proper flush and close calls</td>
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<td>Reduce memory usage of reductions</td>
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