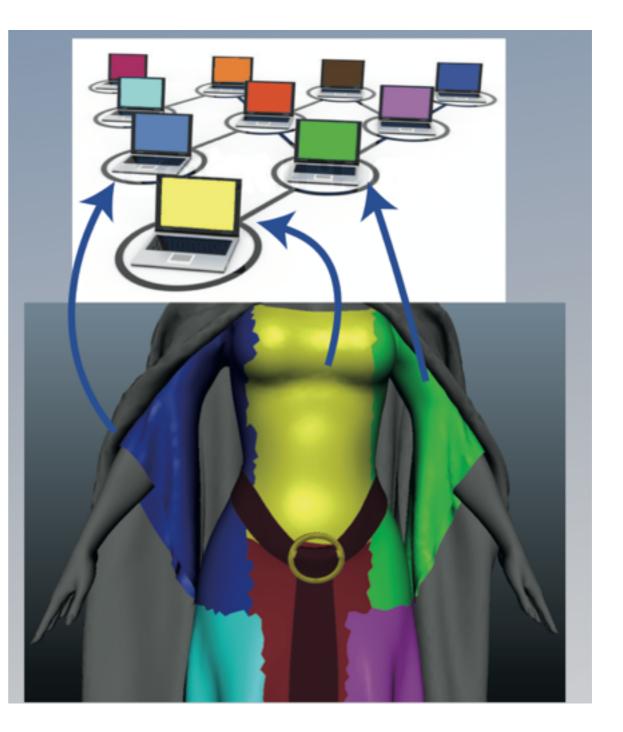
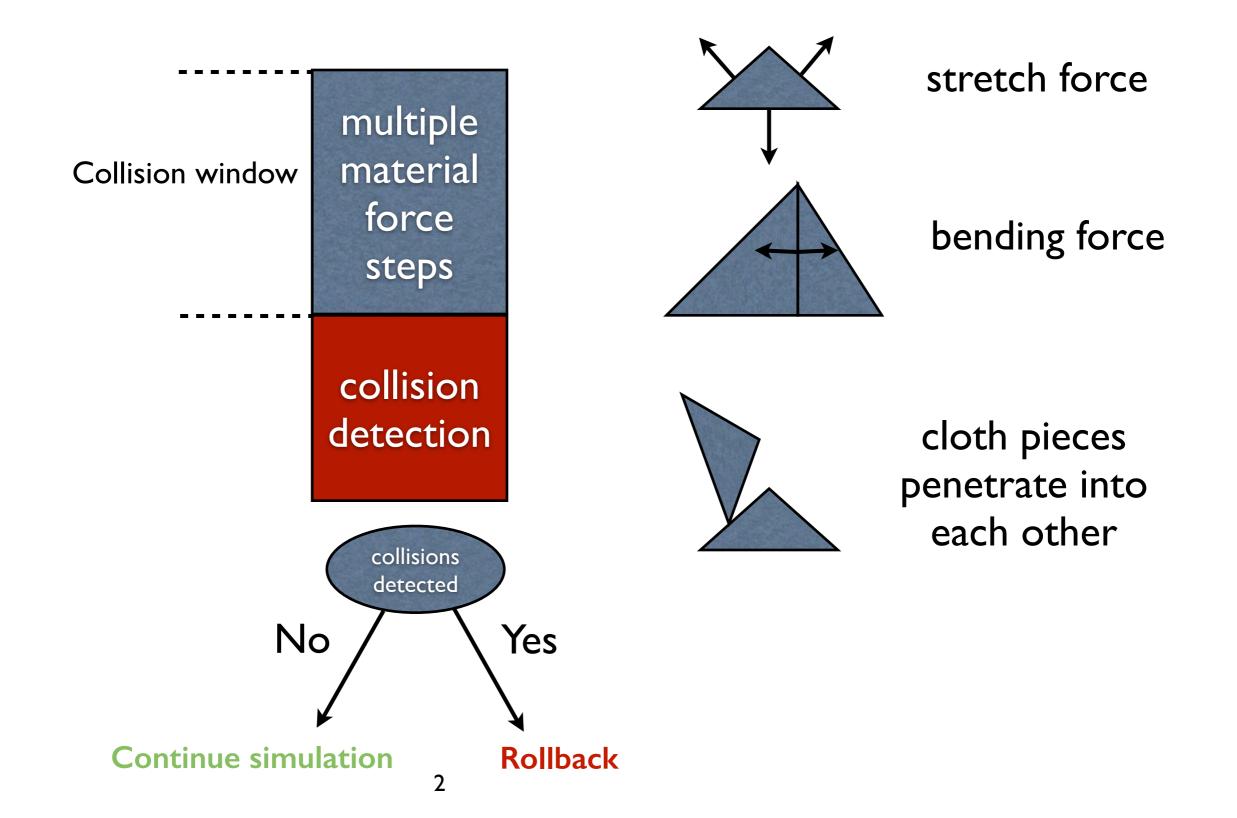
Decomposition

Cloth is decomposed into 2D Triangle mesh

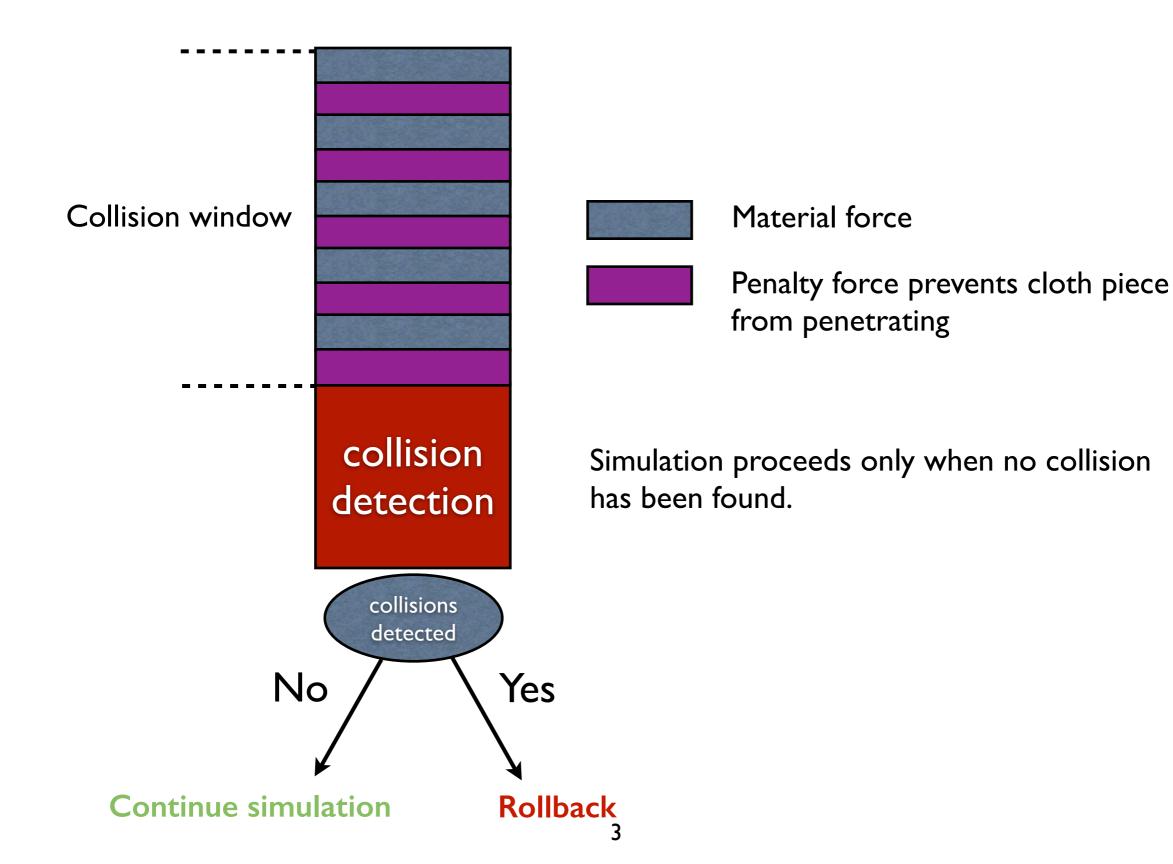
The mesh is partitioned using METIS among work units (a ID chare array)



Overall Flow

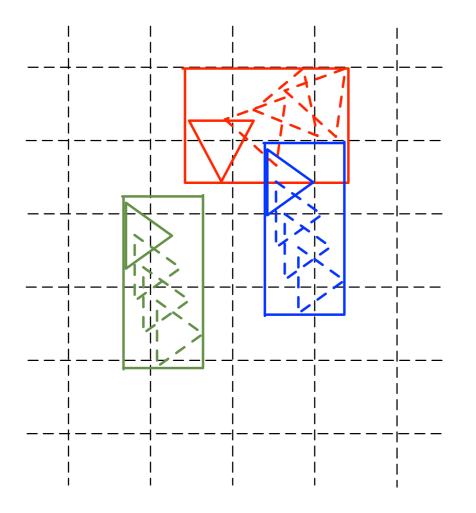


Rollback



Collision Detection

Charm++ collide library for broad phase collision detection*



Create bounding box for the trajectory of each triangle

Result:

Red collides with Blue

Red collides with Green

False Positive

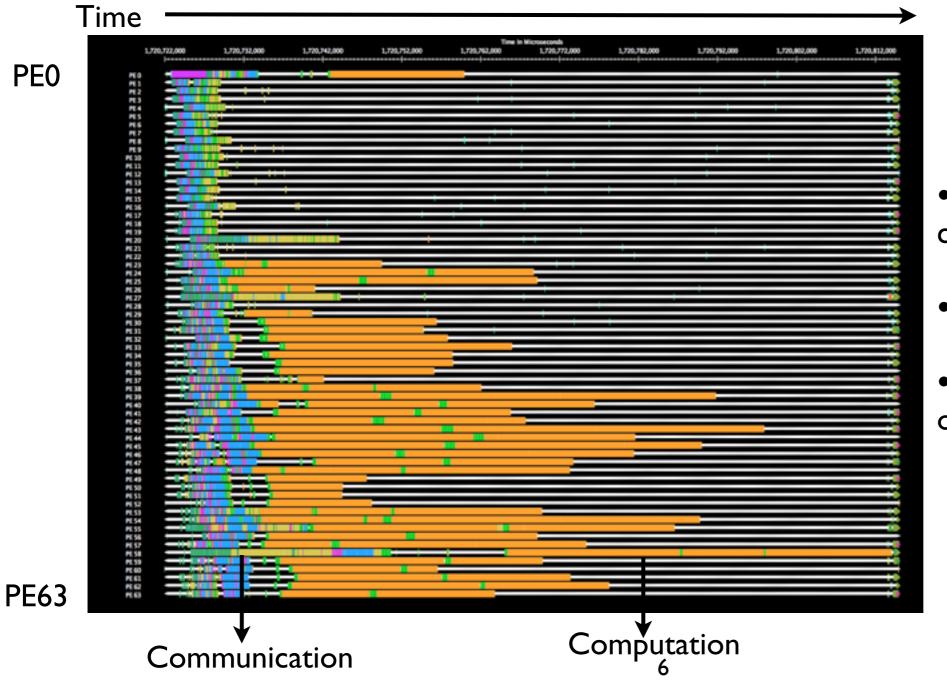
* A Voxel-Based Parallel Collision Detection Algorithm. Orion Lawlor, Laxmikant Kale. ICS02

Narrow Phase Collision Detection

- Filter out the false positive results
- A closer look at the position of each triangle at each time step.
- First, we distribute the potential collision results evenly among all the processors.

Narrow Phase

Timeline view from projections



Computation Imbalance?

•Time spent on each collision pair is different

•Different trajectory length

•Collision happens earlier or later?

Narrow Phase: Principle of Persistence

Take the variation into consideration by profiling

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PE 35 PE 36 PE 37		more
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PE 47		
PE 48 PE 49		
PE 50 PE 51		
PE 52 PE 53		
PE 54		
PE 56		
PE 58		
PE 60		
PE 61 PE 62		
PE 63		

7

Narrow Phase

- Communication Imbalance
- Replicate the data on the heavily loaded processors to a few other processors

Narrow Phase: Effect of Replication

Time In Microseconds 30,525,500 1,730,535,500	1,730,545,500	1,730,555,500	1,730,565,500	1,730,575,50
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Why does replication take long?

- Large message size
- Less data reuse

Narrow Phase: Communication Imbalance

- Let's consider the physical node as an execution unit
- In the same node, processors with less communication can naturally offload the computation work from the processors with more communication.
 - Enabled by asynchronous message driven execution
- **Priority** based execution is important
 - Communication request gets processed as soon as possible

Narrow Phase: Node-level Parallelization

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P3 (M3) P4 (M3)							- 14	_		_		_	
PS (ND) P6 (ND)							-					_	
P7 (M3)						-						_	
P8 (NO) P9 (NO)													
P10 (M3) P11 (M3)								_				_	
P12 (ND) P13 (ND)							-					_	
P34 (M0)													
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P21 (NI) P22 (NI)													_
P23 (ND) P24 (ND)													
P25 (NI)					_								
P26 (NI) P27 (NI)													_
P28 (ND) P29 (ND)													
CommP (NI) P30 (N2)													
P31 (NO)							_						
P32 (N2) P33 (N2)													
P34 (N2) P35 (N2)													_
P36 (N2) P37 (N2)													
P38 (NO)	_						_						
P39 (N2) P40 (N2)				<u></u>									
P41 (N2) P42 (N2)													
P43 (N2) P44 (N2)													
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P45 (ND) P46 (ND)													
P47 (N3) P48 (N3)					_								
P49 (N3) P50 (N3)													
P51 (N0)	-												
P52 (N3) P53 (N3)													
P54 (ND) P55 (ND)													
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P57 (ND) P58 (ND)													
PS9 (N3) CommP (N3)													

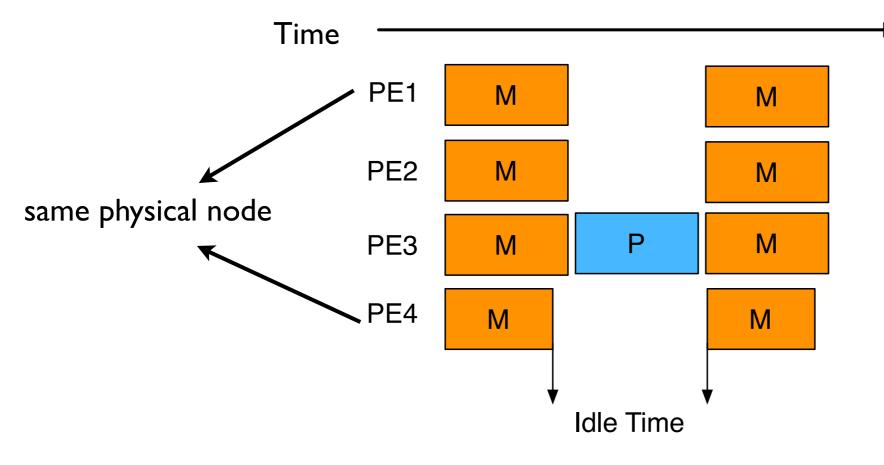
• Communication requests get processed first

• Higher priority

• Different communication computation ratio, but work gets balanced overall

• Processors poll for work as soon as they get idle

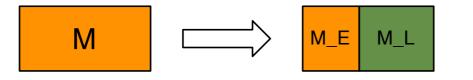
Force Computation



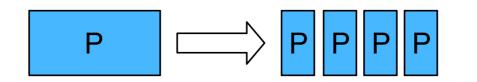
M: one step of material force P: one step of penalty force

- Phase by phase simulation
- Imbalanced distribution of penalty force
 - Collisions only happen in a few regions

Decomposing Force Computation

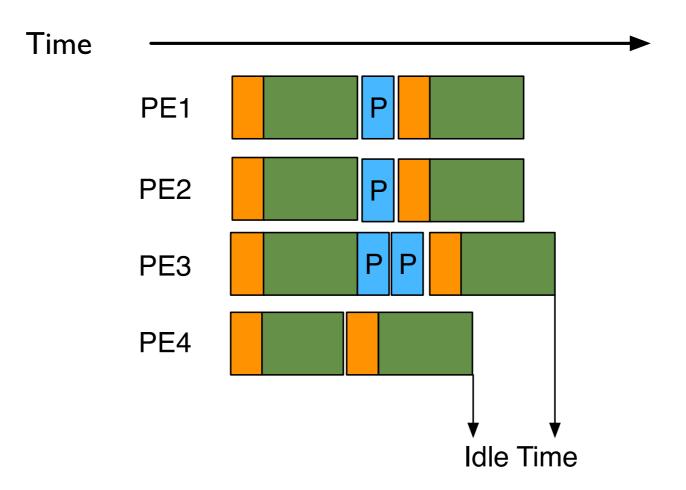


M_E: external computation M_L: local computation



Penalty force decomposition

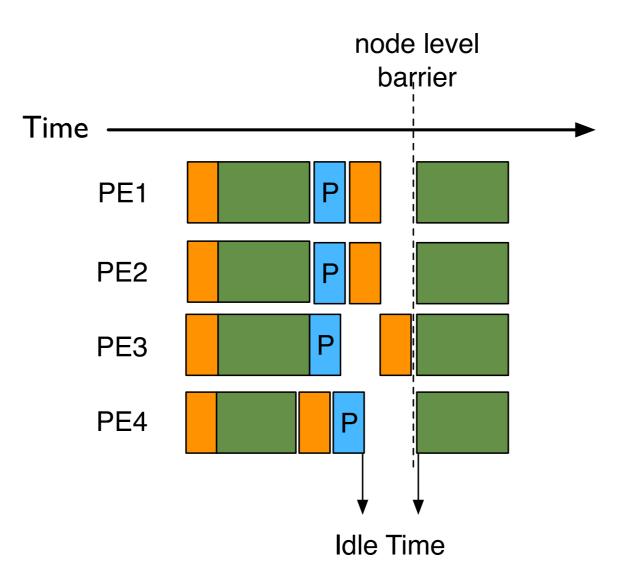
Overdecomposed Force Computation



Other processors can help PE3 with penalty computation except PE4. Why?

The message which invokes the long material force calculation on PE4 comes early!

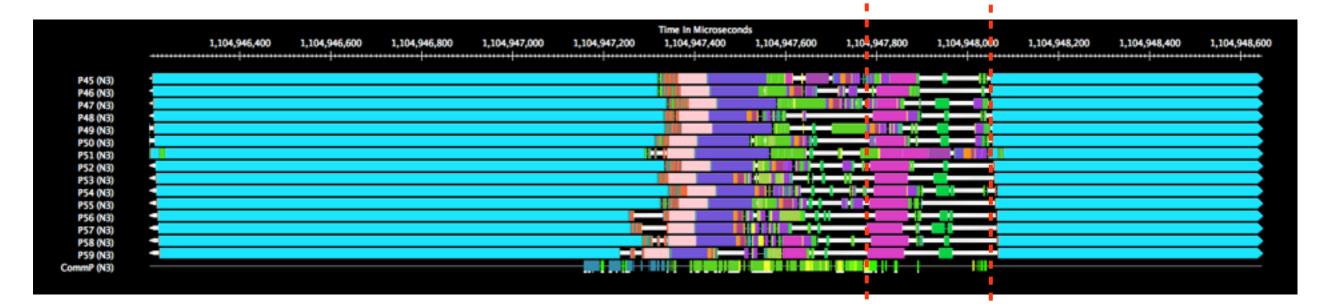
Force Computation



A low cost node level phase barrier to delay the long computation. PE4 can participate to help PE3 with penalty force calculation.

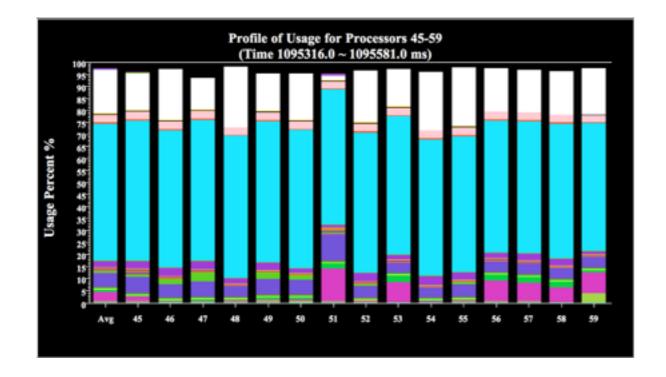
	1,095,383,260	1,095,383,460	1,095,383,660	1,095,383,860	1,095,384,060	Time In Microseconds 1,095,384,260	1,095,384,460	1,095,384,660	1,095,384,860	1,095,385,060	1,095,385,260	1,095,385,460
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Only a few PEs (53,56,57,58,59) help with the Penalty force computation (from PE51).

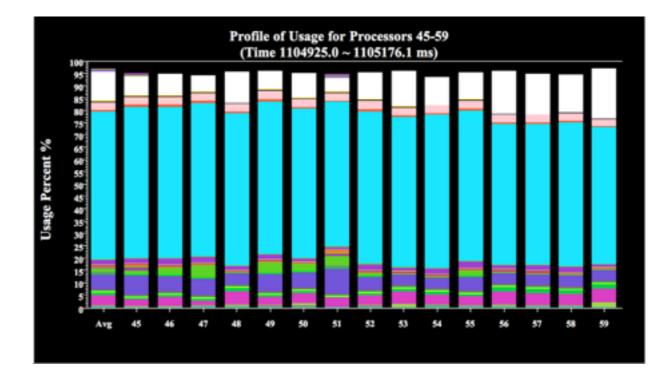


Penalty force computation (from PE51) is divided among all PEs on the node.

Effect of Node-level Phase Barrier

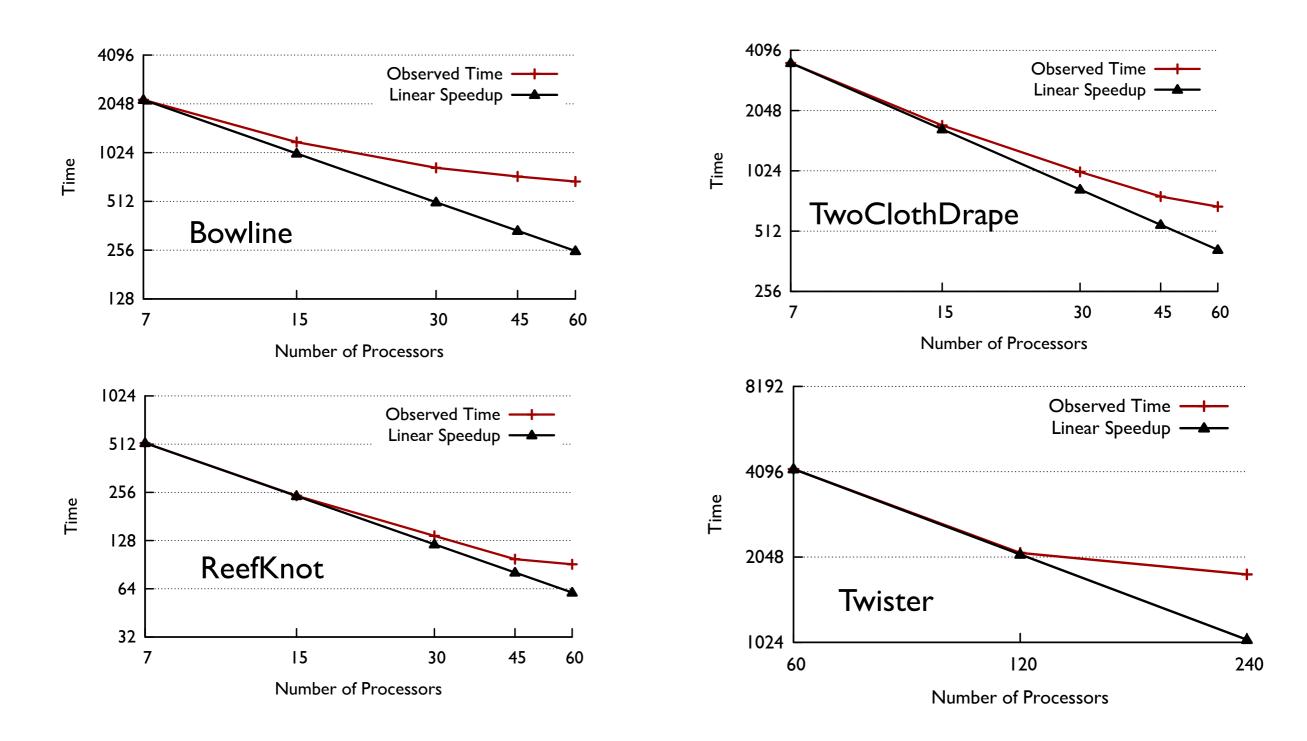


Without node-level phase barrier



With node-level phase barrier

Results



Future Work

